Contribution ID: 68 Type: MCRP-FPGA

Development of Computing Platform based on Transprecision and Inter-GPU Streaming Mechanism for HPC

Email

hanawa@cc.u-tokyo.ac.jp

Number of project members

13

Cygnus: Project code

[CSPP]

Cygnus: Requested [awarded] node-hour product

5,000 [2,800]

Cygnus: Requested [awarded] disk capacity (TB)

15 [10]

Wisteria-O: Project code

[No]

Wisteria: Requested [awarded] node-hour product

[No]

Wisteria: Requested [awarded] disk capacity (TB)

[No]

Primary author(s): Prof. HANAWA, Toshihiro (The University of Tokyo)

Track Classification: High-performance computing system(超高速計算システム分野)