Contribution ID: 45 Type: MCRP-FPGA

## HPC application and system software development on FPGA-GPU combined platform

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Email
```

taisuke@cs.tsukuba.ac.jp

Number of project members

15

Cygnus: Project code

[CCUSC]

Cygnus: Requested [awarded] node-hour product

10,000 [6,930]

Cygnus: Requested [awarded] disk capacity (TB)

[10]

Wisteria-O: Project code

[No]

Wisteria: Requested [awarded] node-hour product

[No]

Wisteria: Requested [awarded] disk capacity (TB)

[No]

Primary author(s): BOKU, Taisuke (University of Tsukuba)

Track Classification: High-performance computing system(超高速計算システム分野)