Contribution ID: 76 Type: MCRP-FPGA

Development of Computing Platform based on Transprecision and Inter-GPU Streaming Mechanism for HPC

Number of project members

11

Cygnus: Project code

[CSPP]

Cygnus: Requested [awarded] number of nodes

[32]

Cygnus: Requested [awarded] node-hour product

[5000]

Cygnus: Requested [awarded] disk capacity (TB)

[15]

OFP: Project code

No

OFP: Requested [awarded] number of nodes

0

OFP: Requested [awarded] node-hour product

0

OFP: Requested [awarded] disk capacity (TB)

Primary author(s): HANAWA, Toshihiro (The University of Tokyo)

Track Classification: High-performance computing system(超高速計算システム分野)