

Accelerating big data analysis using GPU and FPGA

Number of project members

6

Cygnus: Project code

[FPGA4BD]

Cygnus: Requested [awarded] number of nodes

[32]

Cygnus: Requested [awarded] node-hour product

[10000]

Cygnus: Requested [awarded] disk capacity (TB)

[15]

OFP: Project code

No

OFP: Requested [awarded] number of nodes

0

OFP: Requested [awarded] node-hour product

0

OFP: Requested [awarded] disk capacity (TB)

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Track Classification : Computational informatics(計算情報学分野)